

## Session: TH3C

# Mapping of Fields and Waves in Integrated Circuit Structures

*Chair*

**Z. Skvor**

*Czech Technical University*

*Co-Chair*

**R. Ham**

*Consultant*

Non-destructive mapping of the fields and waves in integrated circuit structures is a developing technology with considerable implications in design application, reliability optimization and design verification. Scanning force microscopy, electro-optic, electric field and SAW laser probing are presented.

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1:40 pm - 3:30 pm Thursday, June 20, 1996  
Room 133

